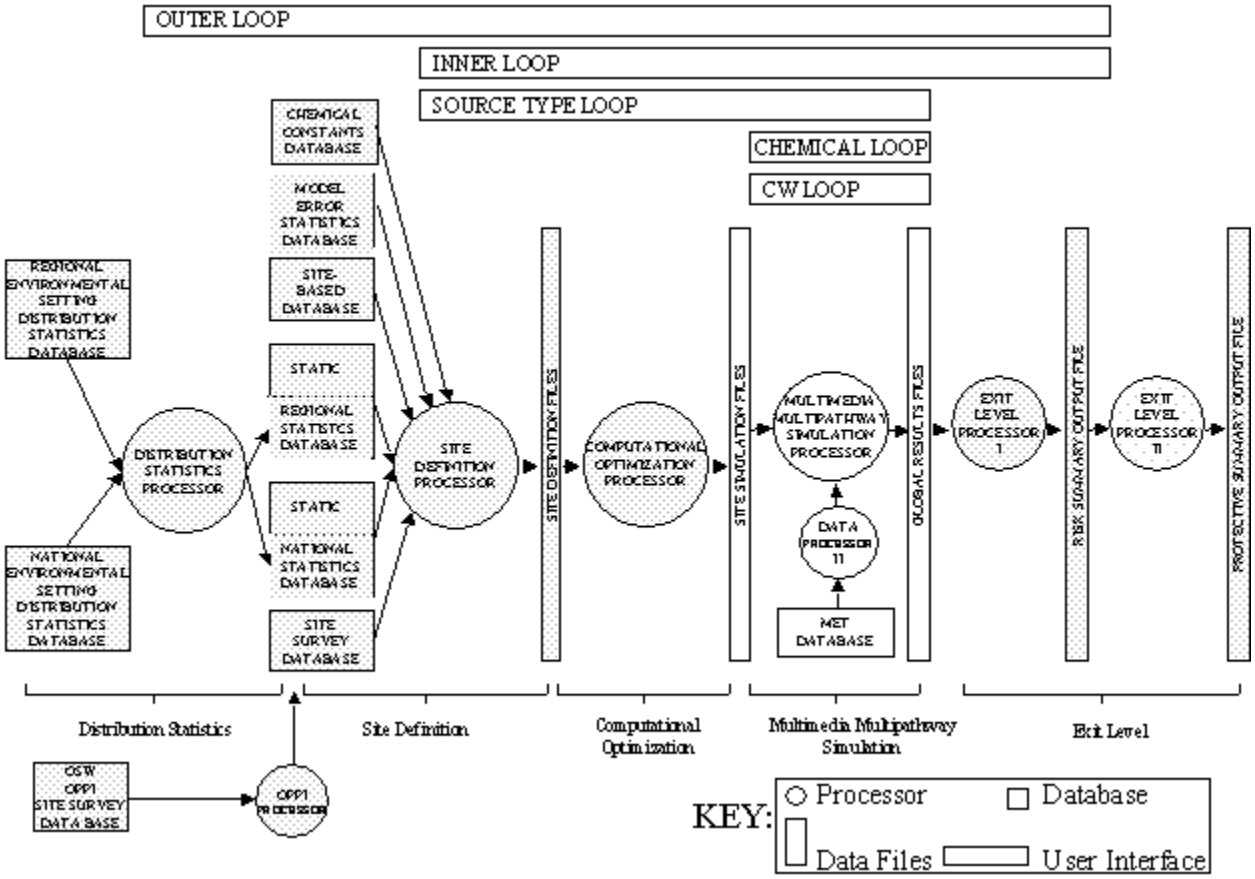

Figure 3.19 View of multimedia multipathway simulation processor.