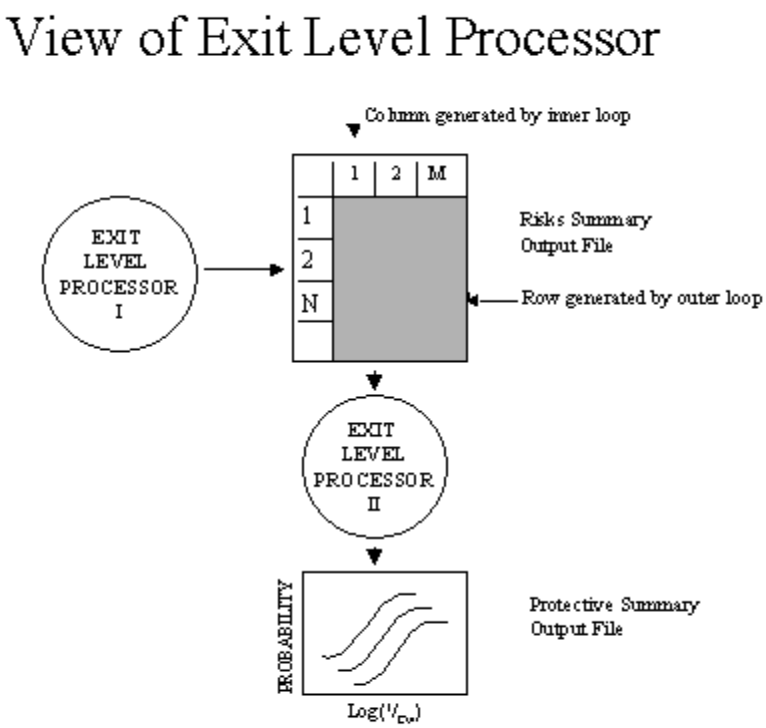

Figure 8.21. View of Exit Level Processor

Figure 3.22 Overview of the exit level processor.