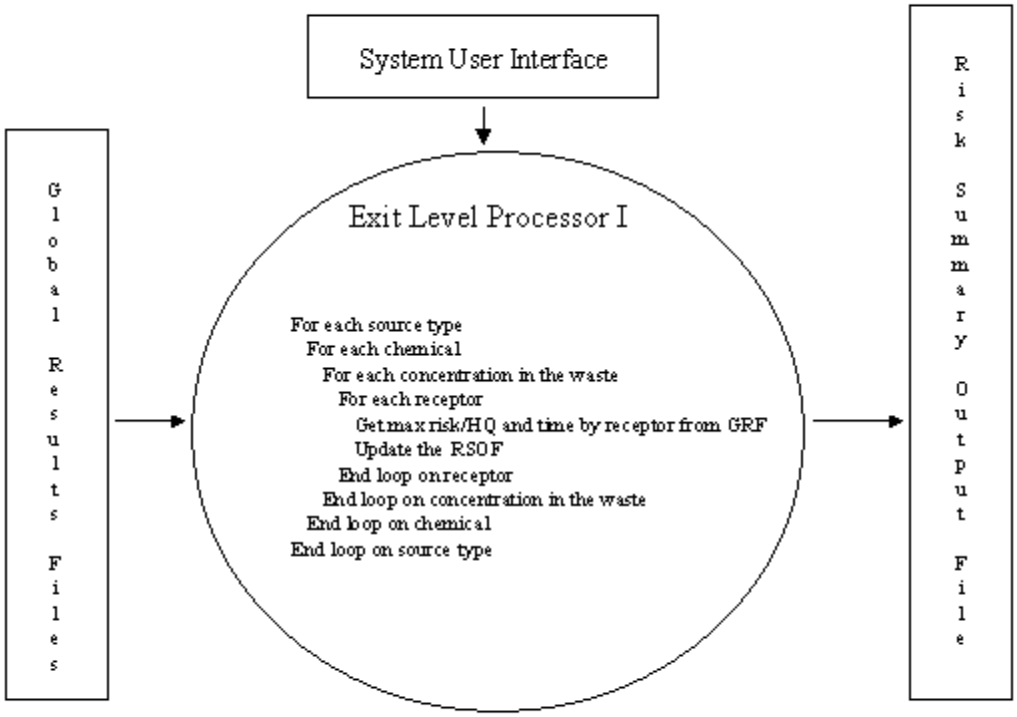

Figure 8.19. Interactions in Exit Level Processor I

Figure 3.23 Interactions in exit level processor I.