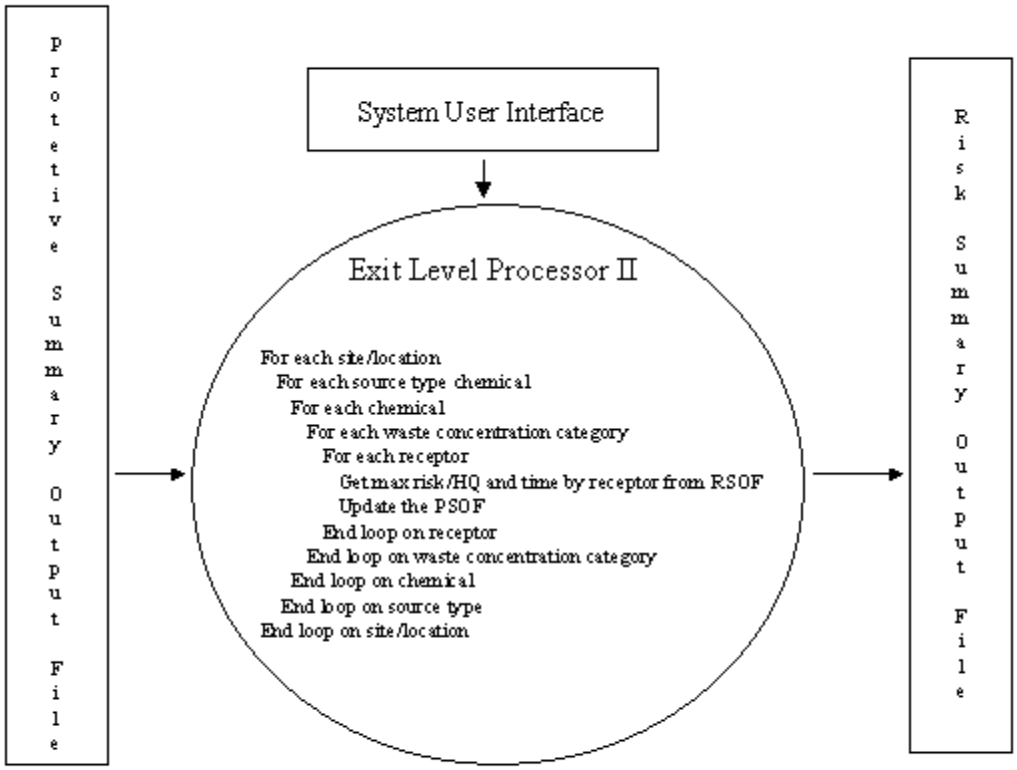

Figure 8.20. Interactions in Exit Level Processor II

Figure 3.24 Interactions in exit level processor II.